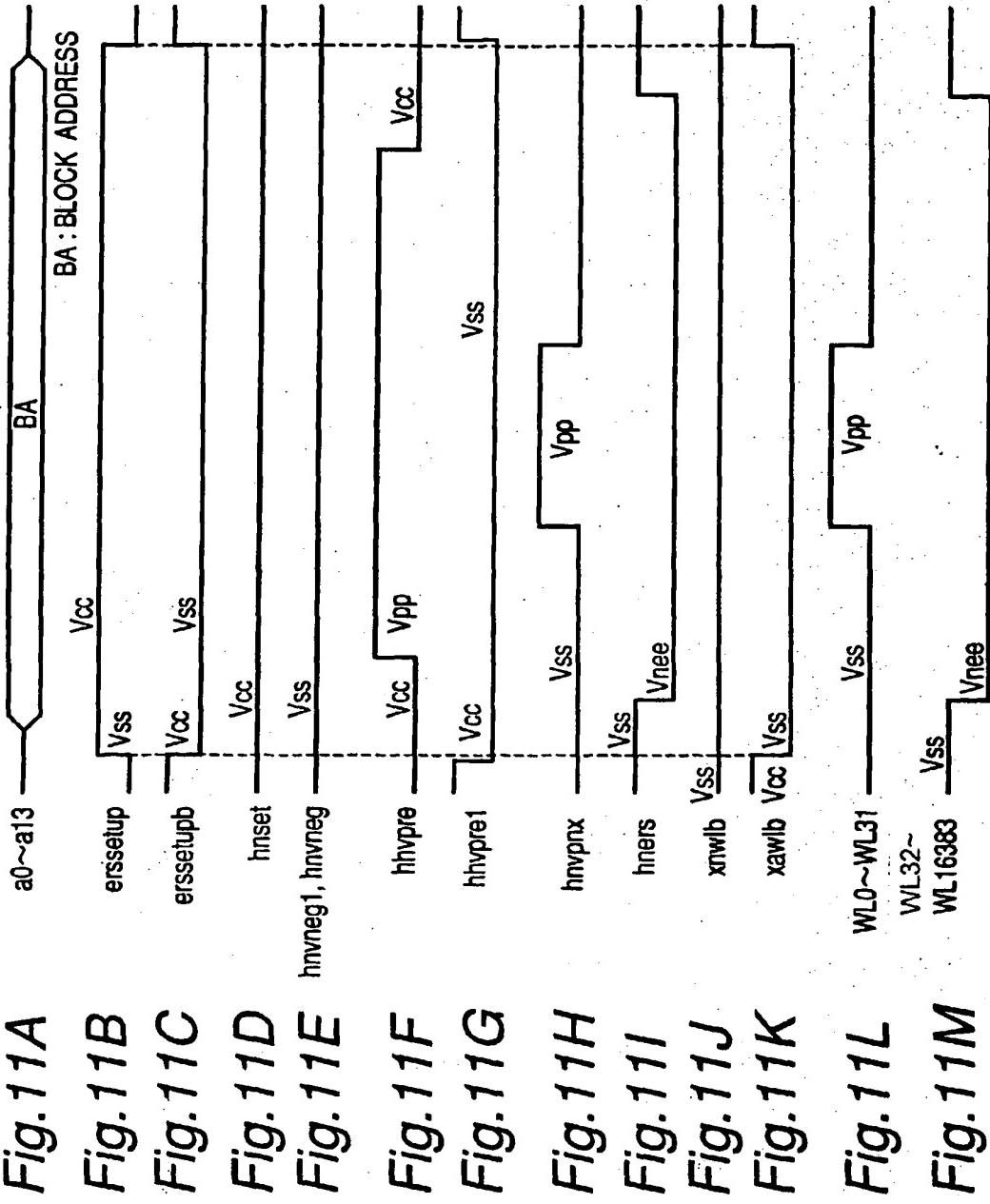


App No.: 09/598,384 Docket No.: 204552018400  
Inventor: Yasuaki HIRANO  
Title: ERASE METHOD FOR NONVOLATILE  
SEMICONDUCTOR STORAGE DEVICE AND ROW  
DECODER CIRCUIT FOR FULFILLING THE METHOD

## REPLACEMENT SHEET



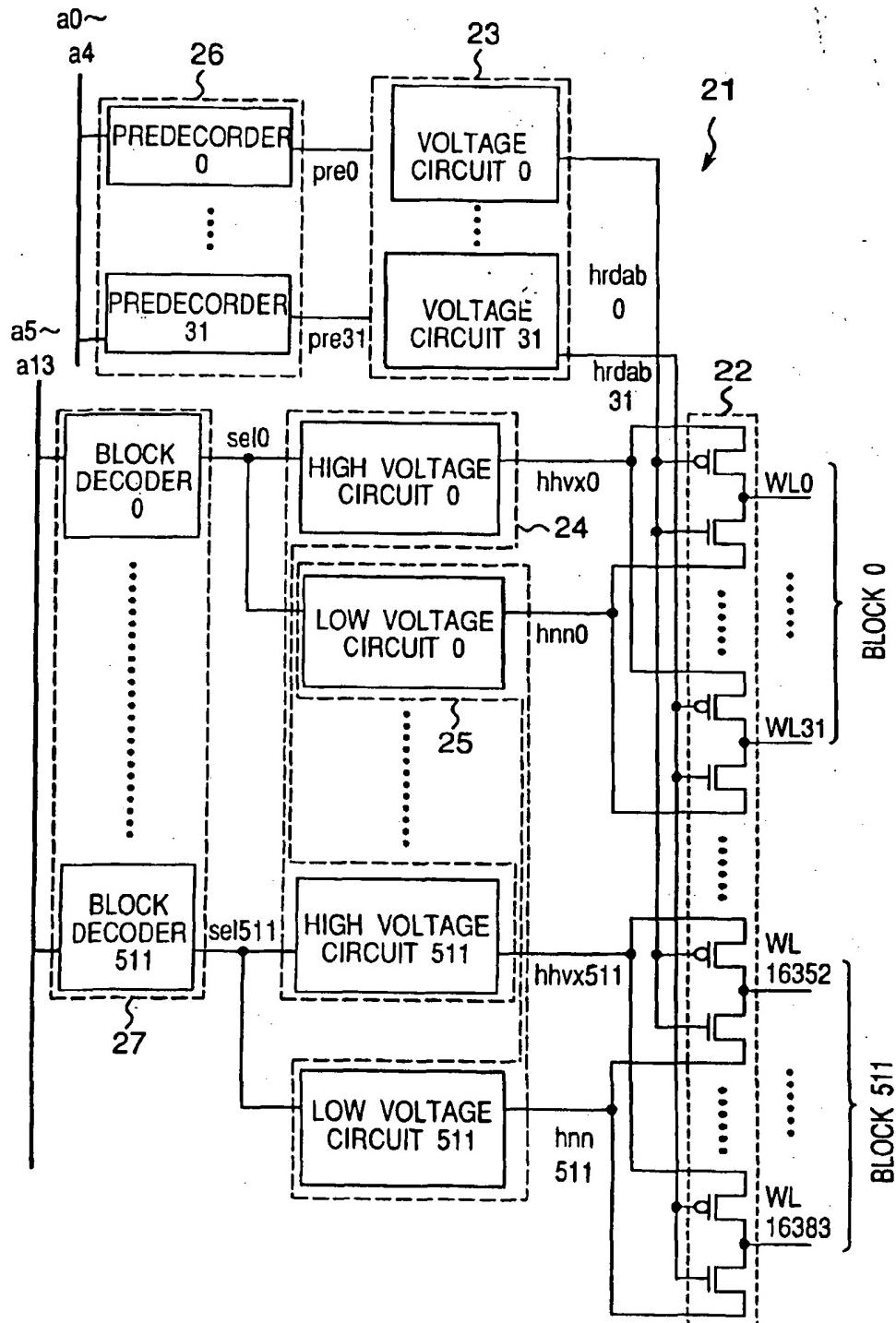
Approved  
10/17/03



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App No.: 09/598,384 Docket N. : 204552018400  
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## Fig. 12 REPLACEMENT SHEET



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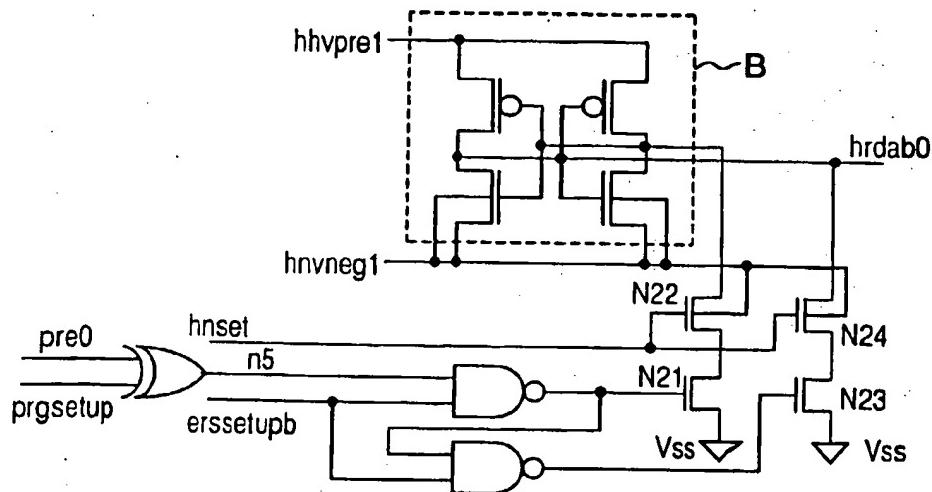
Docket No.: 204552018400

Inv ntor: Yasuaki HIRANO

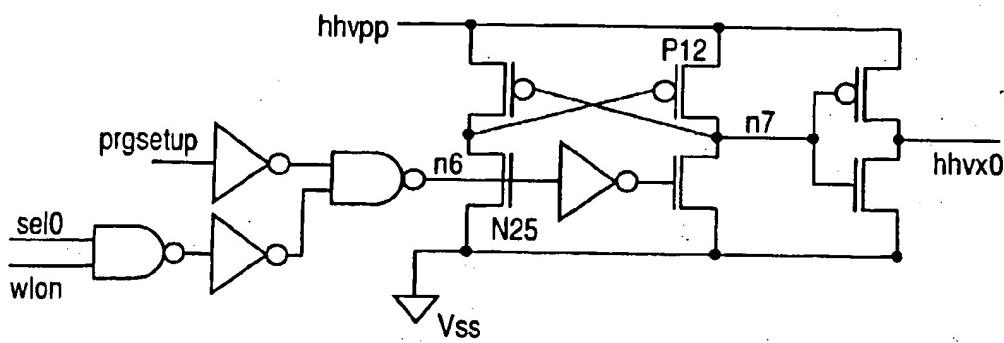
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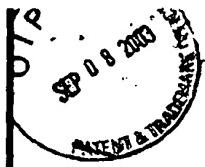
## REPLACEMENT SHEET

*Fig. 13*



*Fig. 14*

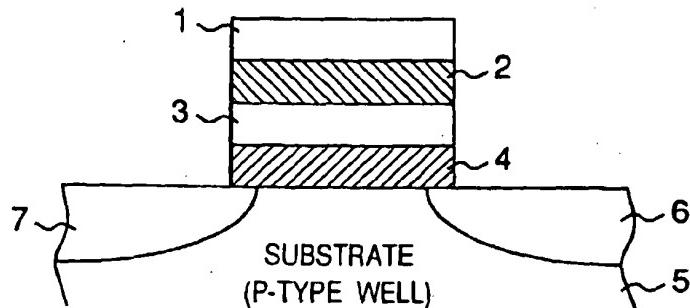




INVENTOR: YASUAKI HIRANO  
DOCUMENT NO.: 204002010700  
Title: ERASE METHOD FOR NONVOLATILE  
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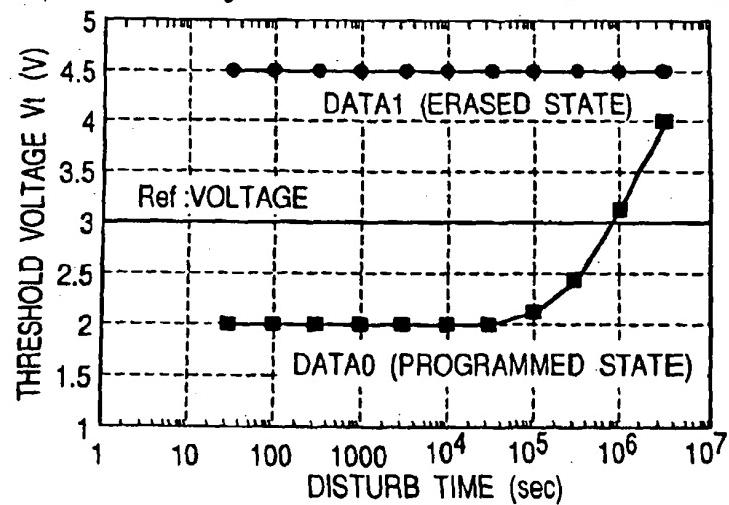
## REPLACEMENT SHEET

*Fig.19 PRIOR ART*



*Fig.20 PRIOR ART*

CONDITIONS :  $V_g = 0V$ ,  $V_d = V_s = \text{FLOATING}$ ,  $V_{\text{sub}} = -8V$





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## REPLACEMENT SHEET

*Fig.21 PRIOR ART*

